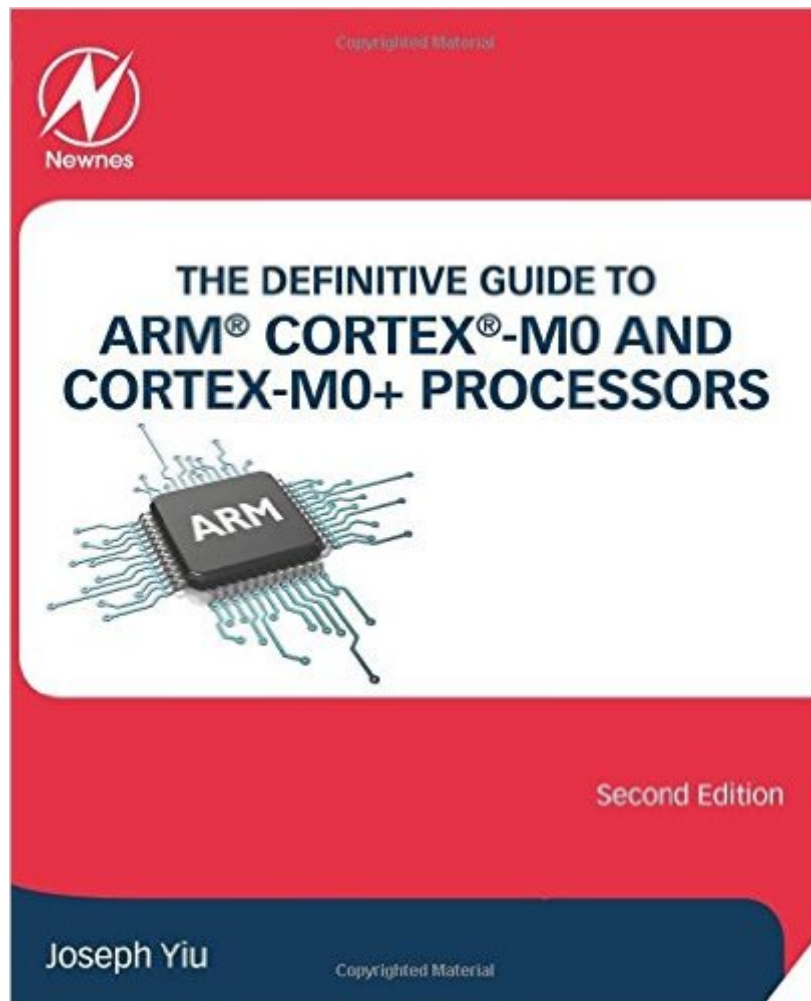# The Definitive Guide To ARMÂ® CortexÂ®-M0 And Cortex-M0+ Processors, Second Edition

Synopsis

The Definitive Guide to the ARM® Cortex®-M0 and Cortex-M0+ Processors, Second Edition explains the architectures underneath ARMâ ™s Cortex-M0 and Cortex-M0+ processors and their programming techniques. Written by ARMâ ™s Senior Embedded Technology Manager, Joseph Yiu, the book is packed with examples on how to use the features in the Cortex-M0 and Cortex-M0+ processors. It provides detailed information on the instruction set architecture, how to use a number of popular development suites, an overview of the software development flow, and information on how to locate problems in the program code and software porting. This new edition includes the differences between the Cortex-M0 and Cortex-M0+ processors such as architectural features (e.g. unprivileged execution level, vector table relocation), new chapters on low power designs and the Memory Protection Unit (MPU), the benefits of the Cortex-M0+ processor, such as the new single cycle I/O interface, higher energy efficiency, better performance and the Micro Trace Buffer (MTB) feature, updated software development tools, updated Real Time Operating System examples using Keilâ„¢ RTX with CMSIS-RTOS APIs, examples of using various Cortex-M0 and Cortex-M0+ based microcontrollers, and much more.Provides detailed information on ARM® Cortex®-M0 and Cortex-M0+ Processors, including their architectures, programming model, instruction set, and interrupt handlingPresents detailed information on the differences between the Cortex-M0 and Cortex-M0+ processorsCovers software development flow, including examples for various development tools in both C and assembly languagesIncludes in-depth coverage of design approaches and considerations for developing ultra low power embedded systems, the benchmark for energy efficiency in microcontrollers, and examples of utilizing low power features in microcontrollers

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Customer Reviews

Pretty good reference. Most of the contents are available on the web already. But, I like having it at my fingertips. There are other thing things I have found in it which weren't on the web which came in handy as an introduction to the topic.

I appreciate the presentation of ARM architecture and rationale behind core design.

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